

PANDIT DEENDAYAL PETROLEUM UNIVERSITY, GANDHINAGAR

SCHOOL OF TECHNOLOGY

Department of Computer Science and Engineering / Department of ICT

Minutes of the Board of Studies Meeting of CE and ICT programme

The meeting of the Board of studies of the Computer Engineering and Information and Communication Technology (ICT) Programme of CSE Department was held on **11th May 2018 at 3:00 pm** in E-Block Committee Room of SOT building PDU. Dr. Tajinder Pal Singh, HOD of CE/ICT Department chaired the meeting. The following members were present in the meeting:

Dr. Tajinder Pal Singh	Director, SOT.	
Dr. D. M. Parikh	Dean, FOET	
Dr. Tajinder Pal Singh	HOD, CSE Dept.	Chairman (BOS)
Computer Engineering		
Dr. Vibha Patel	Professor, VGEC, Chandkheda	External Member
Dr. Asim Banerjee	Professor, DAIICT	External Member
Mr. Indrajit Mitra*	Co-Founder, Jt. Managing Director, Gateway Technolabs	Professional Member
Mr. Harish Chib*	Vice President, Sophos Technologies Pvt Ltd.	Professional Member
Dr. Samir B. Patel	Assistant Professor, CE, PDU	Internal Member
Dr. Manish Chaturvedi	Assistant Professor, CE, PDU	Internal Member
Information and Communication Technology		
Dr. Darshak Thakore	Professor and Head, BVM.	External Member
Dr. Zunnun Narmawala	Associate Professor, Nirma University	External Member
Mr. Chirag Dhebar*	Global Head, Global Consulting Practice, ERP, Transformation Group, TCS	Professional Member
Mr. Amit Rawat	Scientist SC, ISRO, Ahmedabad	Professional Member
Dr. Gangaprasad Pandey	Assistant Professor, ICT, PDU	Internal Member
Dr. Jigar Shah	Assistant Professor, ICT, PDU	Internal Member

Following other members were present in the meeting:

- 1) Dr. Nishant Doshi - Asst. Professor
- 2) Dr. R. Jothi - Asst. Professor
- 3) Dr. Reema Patel - Asst. Professor
- 4) Mr. Santosh Bharti - Lecturer

*Following members could not attend the meeting:

- Mr. Harish Chib (Sophos Technologies Pvt Ltd),
- Mr. Indrajit Mitra (Co-Founder, Jt. Managing Director, Gateway Technolabs),
- Mr. Chirag Dhebar (Global Head, Global Consulting Practice, ERP, Transformation Group, TCS)

The following points were discussed in the meeting:

- Approved the minutes of the previous BOS meeting.

A few points discussed in the previous BOS meeting were not considered.

Semester 5:

- Artificial Intelligence (4-0-0) is still offered in Sem 6 with laboratory (0-0-2).
- Web Technology course with lab is introduced in SEM 5 (0-0-3) for Theory and (0-0-2) for laboratory instead of SEM 6

Semester 6:

- Design of Operating System (3-1-0) course is not considered.
 - Parallel & Distributed computing course (3-1-0) is replaced to Distributed System (4-0-0) in SEM 6.
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Batch : 2016 and 2017.

The detailed syllabus of all the subjects of semester V and VI were discussed and approved.

The organization of elective courses was discussed at length. It was decided to organize the electives track wise to enable students to pursue certain specialization from semester 6 onwards.

The following changes in the outline of curriculum were proposed and approved:

SEM 3:

1. Digital Logic Design laboratory for 1 credit (0-0-2) for CE is added.
2. Signals & Systems laboratory for 1 credit (0-0-2) for ICT is added.
3. Database Management System laboratory (0-0-3) is changed to 0-0-2.
4. Data & File Structure Laboratory (0-0-3) is changed to 0-0-2.

SEM 4:

5. Design and Analysis of Algorithms Lab (0-0-2) is added.
6. Computer Networks lab (0-0-3) is changed to (0-0-2)
7. Object Oriented Modelling & Design (3-0-0) is changed to Object Oriented Concepts & Programming(3-0-0) with laboratory (0-0-2)
8. Object Oriented Concepts & Programming theory and practical for 3 (3-0-0) and 1 (0-0-2) for CE and ICT (2017 batch onwards) is added.

SEM 5:

1. A Subject of Information Security (3-0-0) with lab (0-0-2) is shifted from SEM 7 to SEM 5. This will enable the students to take elective courses belonging to the Security track.
2. Software Engineering subject credit is changed from (4-0-0) to (3-0-0).
3. Web Technology subject credit is changed from (4-0-0) to (3-0-0).
4. Web Technology Lab subject credit is changed from (0-0-3) to (0-0-2).

SEM 6:

1. Distributed system laboratory for 1 credit (0-0-2) for CE is added.
2. Embedded System laboratory and Digital Signal Processing Lab for 1 credit (0-0-2) for ICT is added.

SEM 7:

1. A core subject on Optimization Techniques (3-0-0) is introduced
2. The subject on Machine Learning (3-0-0) with lab (0-0-2) is introduced in place of Big Data Analytics. The Big Data Analytics subject is moved to elective pool under Data science category.
3. The credit of Mini Project is changed from 0-0-4 (2 credits) to 0-0-8 (4 credits).

SEM 8

1. The Comprehensive project II (0-0-38) is offered in three modes:
 - a. Comprehensive Project with Industry (0-0-38) of 19 credits.
 - b. Major Project (0-0-26) of 13 credits with two elective subjects of three credits each. The students are expected to attend elective courses at the institute and carry out the project work in industry for remain days.
 - c. Major Project (0-0-26) of 13 credits with two elective subjects of three credits each. The students are expected to carry out project work in-house with faculty mentor(s).

Further to the above points, the following observations have been noted and incorporated:

1. Two open elective courses namely Programming with Python (3-0-0) and Advanced Java (3-0-0) have been approved. They will be offered during Semester 4 to 6 by Computer Engineering Department. Similarly, the ICT department will offer two open elective courses namely Fundamentals of IOT (3-0-0) and Sensors & Signal Processing (3-0-0) during Semester 4 to 6.
2. The project work based on the Software Engineering and Web Technology subjects should be carried out by the students for better understanding of concepts. The same may be incorporated as a part of internal evaluation.
3. In the Operating systems course the book titled Design of Unix Operating System by Maurice J Bach PHI publisher may be added.
4. In the syllabus of Theory of Computation course, the unit wise tutorial hours should be mentioned.
5. The feedback about curriculum should be taken from the current students. The same may be used as one of the input for updating the curriculum.

Batch : 2018

1. The outline of Computer Engineering and ICT curriculum for 2018 batch were discussed in detail. Based on the discussion, the outlines were refined.
2. The syllabus of semester I and semester II were discussed and approved.

Members of BOS suggested making the changes in ICT curriculum in similar lines of Computer Engineering.

The above points are summarized in the tabular format suggested by the FOET as follows:

Serial number	Year /semester	Course & course code	Addition/deletion of subject/topic	Justification	Remarks if any
1.	2017-18/Sem 3 for CE	Digital Logic Design laboratory	New Lab is added (0-0-2)	To gain hardware knowledge a Lab for Digital Logic Design is required to be added for CE students	
2.	2017-18/Sem 3 for ICT	Signals & Systems laboratory	New Lab is added (0-0-2)	Signals & Systems laboratory is prerequisite for Digital Signal Processing subject for ICT is added.	
3.	2017-18/Sem 3 for CE	Database Management System Laboratory	Credit changed from (0-0-3) to (0-0-2)	To create space for Digital Logic & Design Lab.	

4.	2017-18/Sem 3 for CE & ICT	Data & File Structure Laboratory	Credit changed from (0-0-3) to (0-0-2)	To create space for Digital Logic & Design Lab.	
5.	2017-18/Sem 4	Design & Analysis of Algorithms LAB	New lab is added	Considering the need of the subject Lab is required to be added.	
6.	2017-18/Sem 4	Computer Networks LAB	Credit changed from (0-0-3) to (0-0-2)	To create space for Design & Analysis of Algorithms Lab	
7.	2017-18/Sem 4	Object Oriented Modelling & Design	Course is changed to Object Oriented Concepts & Programming with same credits (3-0-0)	To focus on object oriented programming concepts; Modelling related concepts are incorporated in Software Engineering subject in Semester 5.	
8.	2017-18/Sem 4	Object Oriented Modelling & Design LAB	Course is changed to Object Oriented Concepts & Programming LAB with (0-0-2)	To focus on object oriented programming concepts.	
9.	2016-17/Sem 5	Information security and Lab	Moved from 7 th semester to 5 th Semester	Enable students to take elective courses of Security track.	
10.	2016-17/Sem 5	Software Engineering	Credit change: 4-0-0 to 3-0-0	The course content could be justified for the changed credits; to add information security course	
11.	2016-17/Sem 5	Web Technology	Credit change: 4-0-0 to 3-0-0	The course content could be justified for the changed credits; to add information security course	
12.	2016-17/Sem 5	Web Technology Lab	Credit change: 0-0-3 to 0-0-2	The course content could be justified for the changed credits; to add information security course	
13.	2016-17/Sem 6 for CE	Artificial Intelligence Laboratory	Credit changed from (0-0-3) to (0-0-2)	To create space for Distributed System Lab.	
14.	2016-17/Sem 6 for CE	System Software Laboratory	Credit changed from (0-0-3) to (0-0-2)	To create space for Distributed System Lab.	
15.	2016-17/Sem 6 for CE	Distributed system laboratory	New lab added	To learn the communication in Distributed system.	

16.	2016-17/Sem 6 for ICT	Embedded System laboratory	Combined laboratory of 0-0-3 credit is divided into two subjects with credit of 0-0-2	Due to overloading of two subjects laboratory in ICT Embedded System laboratory and Digital Signal Processing Lab is divided into two separate laboratories.	
17.	2016-17/Sem 6 for ICT	Digital Signal Processing Lab	Combined laboratory of 0-0-3 credit is divided into two subjects with credit of 0-0-2	Due to overloading of two subjects laboratory in ICT Embedded System laboratory and Digital Signal Processing Lab is divided into two separate laboratories.	
18.	2016-17/ Sem 7	Optimization Techniques	Moved from Elective pool as a core subject	more suited as a core course	
19.	2016-17/ Sem 7	Machine Learning with Lab	Moved from Elective pool as a core subject	more suited as core course than Big data analytics	
20.	2016-17/ Sem 7	Big Data Analytics	Moved to elective pool of Data Science category	More suited as an elective course	
21.	2016-17/ Sem 7	Mini Project	Credit change: 0-0-4 to 0-0-8	To make it more rigorous and justify the efforts required for project work	
22.	2016-17/ Sem 8	comprehensive project	Offered as per three models: (i) Comprehensive project (0-0-38); (ii) Major Project (0-0-26) in industry with two elective courses; (iii) Major Project (0-0-26) in-house with two elective courses;	Model 2 and 3 enable students aiming for higher studies to learn a few more advanced course	
23.	2016-17/ Sem 5	All subjects	Detailed syllabus of all the subjects of semester V and VI are approved.	As required	
24.	2016-17/ Sem 5&6	All subjects	Detailed syllabus of all the subjects of semester V and VI are approved.	As required	
25.	2018/ All	All	New Course Outline was discussed and approved	For complying to the AICTE guidelines for curriculum design	
26.	2018/ Sem 1&2	All Subjects	Detailed syllabus of all the subjects of semester I and II are	As required	

			approved.		
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The meeting ended with a Vote of Thanks from Prof. T. P. Singh.

Singh
Sd/-

Dr. T. P. Singh
Chairman BOS (CE and ICT)

27	2016-17	2016-17	2016-17	2016-17	2016-17
28	2016-17	2016-17	2016-17	2016-17	2016-17
29	2016-17	2016-17	2016-17	2016-17	2016-17
30	2016-17	2016-17	2016-17	2016-17	2016-17
31	2016-17	2016-17	2016-17	2016-17	2016-17
32	2016-17	2016-17	2016-17	2016-17	2016-17
33	2016-17	2016-17	2016-17	2016-17	2016-17
34	2016-17	2016-17	2016-17	2016-17	2016-17
35	2016-17	2016-17	2016-17	2016-17	2016-17
36	2016-17	2016-17	2016-17	2016-17	2016-17